

Config. program ■ V1.01
Firmware for BDI3000 ■ V1.06

Date: April 10, 2008

Error Correction

- Cortex-A8: Correct fast/stalled memory access sequences.

Config. program V1.01
Firmware for BDI3000 V1.07

Date: July 9, 2008

Enhancements

- Support for ARM1176 and Cortex-M3 (STM32) added.
- Support for STM32F10xx internal flash programming added.

Config. program V1.01
Firmware for BDI3000 V1.08

Date: Aug. 29, 2008

Enhancements

- Support for Luminary Micro Stellaris LM3S internal flash programming added.
- Support for Cortex-M3 / Cortex-A8 Single Wire Debug Mode added (different firmware).

Config. program ■ V1.02
Firmware for BDI3000 ■ V1.09

Date: Febr. 25, 2009

Enhancements

- Support for Cortex-R4 added
- Support for ARM7 connected via JTAG-AP added (see manual).
- New parameter to select the single step mode added (see manual).
- New Telnet commands INTENA and INTDIS added.

Error Correction

- Correct single step of newer ARMv6/v7 instructions.

Config. program V1.02
Firmware for BDI3000 V1.10

Date: Sept. 29, 2009

Enhancements

- Support for DAP-Lite module added.
- Support for multiple A8/A9 debug components behind one DAP added.
- Support for searching nested ROM tables added.
- APB base address of the debug component can be preset (no ROM table search).
- Download speed via GDB improved.
- Support for a different reset type for the initial power-up reset added (see manual).

Config. program V1.02
Firmware for BDI3000 V1.11

Date: May 17, 2010

Enhancements

- Support for Atmel SAM3U / SAM3S internal flash programming added.
- SWD: Single Wire Output can be routed to a TCP/IP channel (see manual).

Config. program V1.02
Firmware for BDI3000 V1.12

Date: August 20, 2010

Enhancements

- Support for Cortex-M0 added.
- Support for LPC1xxx flash programming added.

Config. program ■ V1.03
Firmware for BDI3000 ■ V1.13

Date: Dec. 23, 2010

Enhancements

- Support for STM32L15 internal flash programming added.
- Support for EFM32 internal flash programming added.
- A8: Correct handling of synchronous watchpoints (step over during a restart).
- Get actual number of implemented break/watch points from DIDR register.

Error Correction

- M3: Correct sequence to set debug control bits (e.g. C_MASKINTS) before resume.
- A8/R4: Correct sequence to set PC before resume (use mov pc,r0 instead of bx r0).

Config. program V1.03
Firmware for BDI3000 V1.14

Date: April 1, 2011

Enhancements

- Support for Cortex-M4 added.
- Support for Freescale Kinetis internal flash programming added.
- New EXEC init list entry added (see manual).
- Special DAPPC configuration parameter added for TI processors (see manual).
- New Telnet MMU command allows to disable / enable the MMU.
- New PMM (Physical Memory Mapped) register type accesses memory always with disabled MMU.
- The BDI no longer automatically steps over a GDB set breakpoint at the current PC. This allows to use the GDB „jump“ command.

Error Correction

- A8/R4: Correct setting of instruction HWBP (Byte Select in BCR) for Thumb / Big Endian code.

Config. program V1.03
Firmware for BDI3000 V1.15

Date: November 29, 2011

Enhancements

- Support for STM32F2 and STM32F4 internal flash programming added.