

Config. program ■ V1.01
Firmware for BDI3000 ■ V1.10

Date: May 7, 2008

Enhancements

- The GDB register packet has been changed to support recent GDB versions. Floating-point values are now transferred as 8 bytes instead of the older 12 bytes format.
- The BREAKMODE command now has a new option NOUHE. The BDI normally inserts HALT instructions as its breakpoint instruction. In order to ensure that breakpoints are processed correctly when they are encountered in user-mode code, the BDI normally sets the CSR[UHE] bit to allow user-mode execution of the halt instruction. If NOUHE is defined, the BDI no longer sets CSR[UHE] and a HALT instruction in user-mode leads to an Illegal instruction exception that can be handled by the operating system or target resident debugger.
- A new register type (CMMx) has been added. This allows to define memory mapped registers which are based on an address stored in a control register. For more info about this new register type look at the manual and the mcf5485.def register definition file.

Error Correction

- Prior to firmware version 1.10, the Telnet TLB/WTLB commands would modify the MMUAR, MMUTR, MMUDR registers to perform the operation, but would not restore them. Now the BDI will save the existing values of these registers and restore them after the operation is completed.

Config. program ■ V1.03
Firmware for BDI3000 ■ V1.11

Date: August 5, 2011

Enhancements

- Download speed via GDB improved.
- Maximal number of software breakpoints increased to 64.
- Support for new CPU types added.