

Config. program      ■ V1.01  
Firmware for BDI3000   ■ V1.18

Date: March 19, 2008

## Enhancements

- Telnet binary display of FP registers added.
- Flash programming supports 16-bit byte-swapped systems for AM29BX16 and MIRRORX16.

Config. program      ■ V1.02  
Firmware for BDI3000   ■ V1.19

Date: Febr. 18, 2009

## Enhancements

- Support for PPC464 core added (AMCC PowerPC 460SX)
- New MT Telnet command for a simple bus/memory test added.

Config. program  V1.02  
Firmware for BDI3000  V1.20

Date: January 12, 2010

## Enhancements

- Support for Titan HPC (APM83290) added.
- Download speed via GDB improved.
- New init list entries for memory read (RM8, RM16, RM32) added.
- New init list entry STLB added (see manual).
- Maximal number of software breakpoints increased to 64.

Config. program      ■ V1.03  
Firmware for BDI3000 ■ V1.21

Date: Febr. 22, 2011

## Enhancements

- Support for APM86290 added.
- Initial support for PPC476 core added.
- IBM / RiscWatch like multi-core TAP handling added.
- New configuration parameter MEMDELAY added (see manual).
- New Telnet „sync“ command added that tries to synchronize the BDI with the target processor.

Config. program  V1.03  
Firmware for BDI3000  V1.22

Date: July 28, 2011

## Enhancements

- Improved PPC476 support.
- Multi-Core support for PPC476 (LSI ACP3448) added (see manual).
- New PPC476 Multi-core related Telnet commands added (see manual).