

Config. program V1.00
Firmware for BDI2000 V1.01
Logic for BDI2000 V1.02



Date: Dec. 10, 2001
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Enhancements

- New configuration parameter (STARTUP) added, see user's manual for more information.
- The GDB „monitor“command can be used to access BDI Telnet commands.
- The RS232 port of the target can be routed via the BDI to a TCP/IP channel, see user's manual.
- The address range used for MMU default translation has been increased to 128MB.
- An alternate single step mode has been implemented. The alternate mode uses a hardware break-point to implement single step.

Error Correction

- The erase time-out for AMD flashes has been increased. This was necessary because of long chip erase time.

Config. program V1.00
Firmware for BDI2000 V1.02
Logic for BDI2000 V1.02



Date: April 12, 2002

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Enhancements

- Support for MPC7455 added.
- The new Telnet command RDUMP writes the values of all user defined registers to a file on the host.
- New configuration parameter (WAKEUP) added to support targets that need some wake-up time after the BDI releases COP-HRESET.

Error Correction

Config. program ■ V1.01
Firmware for BDI2000 ■ V1.03
Logic for BDI2000 ■ V1.03



Date: Nov. 7, 2002
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Enhancements

- Processing of GDB monitor command improved.
- Support for AMD MirrorBit flash added.
- If the GDB monitor command is used to download an image via TFTP („mon load“), the BDI sends continuously dots to GDB to prevent a command time-out.
- A List of valid memory ranges can be defined to avoid invalid memory accesses via the JTAG interface (MMAP entry in the init list).
- The Windows setup tool is now a WIN32 application.
- BDI Firmware programming time improved for the Linux setup tool (bdisetup).
- Burst reads via COP/JTAG can be generally disabled if necessary (NOBURSTREAD option).
- New init list entry (TSIZ8) added to disable burst accesses for a memory region.
- New Telnet commands (BD and BDT) allows to set a data breakpoint (via DABR).

Error Correction

Config. program V1.02
Firmware for BDI2000 V1.04
Logic for BDI2000 V1.03



Date: March 7, 2003
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Enhancements

- Support for network configuration via BOOTP added.
- New init list entries allow to execute memory read accesses.
- Support for 32 bit wide flash chips added.
- New Telnet command added to unlock multiple (same size, continuous) flash sectors with one command.
- Improved Telnet „erase“ command to erase multiple (same size, continuous) flash sectors with one command.
- Improved Telnet interface with history buffer and line editing.
- Support for MPC7455 Rev.3 added.
- Support for MPC7457 Rev.1 added.

Note:

The BDI's ethernet MAC address will change with this release.
The setup tool displays the new MAC address.

Config. program V1.02
Firmware for BDI2000 V1.05
Logic for BDI2000 V1.03



Date: April 4, 2003
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Enhancements

- L2 cache coherent memory access added. No need to activate cache flush when the L2 cache is enabled while debugging. See also description of the CACHE parameter in the setup program.
- New Telnet command „DCACHE“ displays the L1 data cache content.
- New Telnet command „L2CACHE“ displays the L2 cache content.
- The Telnet command „CONFIG“ allows to change the name of the configuration file name. The new name is stored in the BDI's flash and a boot sequence is started.

Config. program V1.02
Firmware for BDI2000 V1.06
Logic for BDI2000 V1.03



Date: April 28, 2003
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Enhancements

- Speed optimized JTAG sequences.

Error Correction

- Using COP HALT command instead of FREEZE command solves some memory access problems.

Config. program V1.03
Firmware for BDI2000 V1.07
Logic for BDI2000 V1.03



Date: March 26, 2004
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Enhancements

- Support for MPC7447A added.
- The file names in the configuration file maybe relative to the configuration file path.
- The Telnet command „reset“ is now synchronous. This allows the use of it via a GDB monitor command. Also the handling of GDB monitor commands has been improved.
- The Telnet command „CONFIG“ allows to change the name and IP address of the configuration file. The new name and IP address is stored in the BDI's flash and a boot sequence is started.
- New Telnet DELAY command added. Maybe useful when working with GDB command scripts.
- A data watchpoint (if supported by the processor) can be set via the GDB „watch“, „rwatch“ and „awatch“ commands (supported with GDB V6.0).

Error Correction

- Instruction step of lmw,stmw,lsw and stsw works now correct.

Config. program V1.03
Firmware for BDI2000 V1.08
Logic for BDI2000 V1.04



Date: October 12, 2004
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Enhancements

- The Telnet command „CONFIG“ allows to change all BDI network parameters. The new parameters are stored in the BDI's flash and a boot sequence is started.
- A new configuration parameter (QACK) allows to force the QACK pin low via the BDI.
- The page table search algorithm now also supports systems where the L1 page table entries are physical addresses (NetBSD).
- The flash unlock command has more features (see manual).
- Telnet history buffer no longer filled with repeated commands.

Error Correction

- Programming MirrorBit flash in x16 mode works now also with workspace.

Config. program ■ V1.04
Firmware for BDI2000 ■ V1.09
Logic for BDI2000 ■ V1.05



Date: Sept. 23, 2005
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Enhancements

- Support for MPC7448 added.
- Default kernel translation range has been increased to 256 MB.
- The Windows setup tools now allows to select a COM port in the range 1 - 19.

Error Correction

- For virtual to physical address translation, also BAT 4 - 7 are searched when present.
- Correct address translation of GDB requested memory blocks that crosses a page boundary.

Config. program V1.05
Firmware for BDI2000 V1.10
Logic for BDI2000 V1.05



Date: July 28, 2006
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Enhancements

- Support for MPC8641 / MPC8641D added.
- Enhanced flash erase support via configuration file (see user's manual).
- New option V to the BREAKMODE parameter added (see user's manual).

Config. program V1.05
Firmware for BDI2000 V1.11
Logic for BDI2000 V1.05



Date: Febr. 23, 2007
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Enhancements

- New flash algorithm types added (S29M64X8, S29M32X16).
- The MirrorBit algorithm uses the write buffer also when no workspace is defined.
- New RECONNECT option to the DEBUGPORT parameter added. When present, a Telnet/GDB (TCP) connection request to an already open TCP channel is accepted if the host IP is the same as the initial one. The old TCP channel will be closed immediately.
- New init list entry WREG PC added.
- Support for additional devices on the JTAG scan chain added.
- Support for MPC8641 Rev.2 added.

Config. program V1.05
Firmware for BDI2000 V1.12
Logic for BDI2000 V1.05



Date: Sept. 28, 2007
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Enhancements

- Telnet IAC option handling improved.
- Telnet accepts now multiple commands separated by a semi-colon on one line.
- Display of instruction cache content added.

Config. program V1.05
Firmware for BDI2000 V1.13
Logic for BDI2000 V1.05



Date: January 11, 2008
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Enhancements

- MMU: Support for extended block size mask (BATU[XBL]) added.
- Display decoded instruction cache content.

Config. program V1.05
Firmware for BDI2000 V1.14
Logic for BDI2000 V1.05



Date: May 2, 2008
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Enhancements

- Support for MPC8610 added.
- Hex display of FP register content via Telnet added.

Error Correction

- 7448: Correct access to HID1 and ICTRL

Config. program V1.05
Firmware for BDI2000 V1.15
Logic for BDI2000 V1.05



Date: April 22, 2009

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Error Correction

- 64-bit flash programming with workspace did not work at all. This has been fixed now.