

Config. program V1.01
Firmware for BDI2000 V1.01
Logic for BDI2000 V1.00



Date: February 6, 2004
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Enhancements

- New configuration parameter (WAKEUP) added to support targets that needs some wake-up time after a reset.
- The Telnet command „CONFIG“ allows to change the name and IP address of the configuration file. The new name and IP address is stored in the BDI's flash and a boot sequence is started.
- The file names in the configuration file maybe relative to the configuration file path.
- The Telnet command „reset“ is now synchronous. This allows the use of it via a GDB monitor command. Also the handling of GDB monitor commands has been improved.
- New Telnet DELAY command added. Maybe useful when working with GDB command scripts.
- New configuration parameter (REGLIST) added (see user's manual).

Config. program V1.01
Firmware for BDI2000 V1.02
Logic for BDI2000 V1.00



Date: May 31, 2005
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Enhancements

- The Telnet command „CONFIG“ allows to change all BDI network parameters. The new parameters are stored in the BDI's flash and a boot sequence is started.
- The flash unlock command has more features (see user's manual).
- The erase list accepts a new UNLOCK option (see user's manual).
- Telnet history buffer no longer filled with repeated commands.
- Support for FP and CP1 registers added
- A new configuration option has been added that allows connecting with a GDB built for MIPS32.

Config. program V1.02
Firmware for BDI2000 V1.03
Logic for BDI2000 V1.00



Date: July 12, 2006
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Enhancements

- Enhanced flash erase support via configuration file (see user's manual).
- The Windows setup tools now allows to select a COM port in the range 1 - 19.
- The Telnet CONFIG command displays the firmware version.
- The Telnet BD (set data breakpoint) command accepts now a [<mask>] parameter.
- Improved Telnet history buffer handling.

Error Correction

- Correct handling of Little Endian systems.

Config. program V1.02
Firmware for BDI2000 V1.05
Logic for BDI2000 V1.00



Date: May 13, 2008
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Enhancements

- New configuration parameter (POWERUP) added to support a target that needs some wake-up time after a power-up.
- The MirrorBit algorithm uses the write buffer also when no workspace is defined.
- New RECONNECT option to the DEBUGPORT parameter added. When present, a Telnet/GDB (TCP) connection request to an already open TCP channel is accepted if the host IP is the same as the initial one. The old TCP channel will be closed immediately.
- Telnet accepts now multiple commands separated by a semi-colon on one line.
- Support for low level scan chain configuration added (see user's manual).
- Telnet IAC option handling improved.
- New option JTAGDELAY in [TARGET] section added (see user's manual).
- Support for Multi-Core debugging added. Concurrent debugging of up to 4 daisy chained MIPS64 cores with different GDB sessions is now possible.
- Support for Cavium Networks OCTEON MIPS64 processors added (cnMIPS core).

Config. program V1.02
Firmware for BDI2000 V1.06
Logic for BDI2000 V1.00



Date: June 30, 2008

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Enhancements

- Concurrent debugging of up to 8 cores.
- Improved multi-core related Telnet commands (see user's manual)

Config. program V1.02
Firmware for BDI2000 V1.07
Logic for BDI2000 V1.00



Date: Nov. 26, 2009

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Enhancements

- New Telnet „state“ command added. Shows information about all cores.
- Faster flash programming with workspace.
- Download speed via GDB improved.
- Maximal number of software breakpoints increased to 64.

Config. program V1.02
Firmware for BDI2000 V1.08
Logic for BDI2000 V1.00



Date: April 7, 2011
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Enhancements

- Support for Cavium CNMIPS II processor added.
- Support for Netlogic XLP processor added.
- New init list entries to access CP1, CP2 and XLP-CTR registers added.
- Experimental support for „Non-stop Multi-Threaded Debugging in GDB“ added (see manual).

Config. program V1.02
Firmware for BDI2000 V1.09
Logic for BDI2000 V1.00



Date: May 31, 2011
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Enhancements

- New WREG init list entry allows to set PC, HI and LO registers.
- New MCDEBUG parameter allows to make use of the cnMIPS Multicore Debug features.
- Flashprogramming now also via xkphys segment possible (64-bit address arithmetic).
- Hard reset time down to 1 ms is supported now to handle a XLP reset requirement.

Error Correction

- Correct bypass check for systems with more than 24 JTAG devices on the scan chain.
- Display up to 128 TLB's. Was limited to 64 before.

Config. program V1.02
Firmware for BDI2000 V1.10
Logic for BDI2000 V1.00



Date: July 22, 2011
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Enhancements

- Workaround for CN63xx v2.1/v2.2 EJTAG errata implemented.
- Using FASTDATA mode for CNMIPS II and XLP targets. This improves download/programming speed when using a WORKSPACE.

Error Correction

- Make all flash target algorithm position-independent. This fixes the problem that some of the programming algorithm failed when using a WORKSPACE.