

Config. program ■ V1.01
Firmware for BDI3000 ■ V1.05

Date: May 13, 2008

Enhancements

- New configuration parameter (POWERUP) added to support a target that needs some wake-up time after a power-up.
- New RECONNECT option to the DEBUGPORT parameter added. When present, a Telnet/GDB (TCP) connection request to an already open TCP channel is accepted if the host IP is the same as the initial one. The old TCP channel will be closed immediately.
- Support for low level scan chain configuration added (see user's manual).
- New option JTAGDELAY in [TARGET] section added (see user's manual).
- Support for Multi-Core debugging added. Concurrent debugging of up to 4 daisy chained MIPS64 cores with different GDB sessions is now possible.
- Support for Cavium Networks OCTEON MIPS64 processors added (cnMIPS core).

Config. program V1.01
Firmware for BDI3000 V1.06

Date: June 30, 2008

Enhancements

- Concurrent debugging of up to 16 cores.
- Improved multi-core related Telnet commands (see user's manual)

Config. program ■ V1.02
Firmware for BDI3000 ■ V1.07

Date: Nov. 26, 2009

Enhancements

- New Telnet „state“ command added. Shows information about all cores.
- Faster flash programming with workspace.
- Download speed via GDB improved.
- Maximal number of software breakpoints increased to 64.

Config. program ■ V1.03
Firmware for BDI3000 ■ V1.08

Date: April 7, 2011

Enhancements

- Maximal number of parallel handled cores increased to 32.
- Support for Cavium CNMIPS II processor added.
- Support for Netlogic XLP processor added.
- New init list entries to access CP1, CP2 and XLP-CTR registers added.
- Experimental support for „Non-stop Multi-Threaded Debugging in GDB“ added (see manual).

Config. program V1.03
Firmware for BDI3000 V1.09

Date: May 31, 2011

Enhancements

- New WREG init list entry allows to set PC, HI and LO registers.
- New MCDEBUG parameter allows to make use of the cnMIPS Multicore Debug features.
- Flashprogramming now also via xkphys segment possible (64-bit address arithmetic).
- Hard reset time down to 1 ms is supported now to handle a XLP reset requirement.

Error Correction

- Correct bypass check for systems with more than 24 JTAG devices on the scan chain.
- Display up to 128 TLB's. Was limited to 64 before.

Config. program V1.03
Firmware for BDI3000 V1.10

Date: July 22, 2011

Enhancements

- Workaround for CN63xx v2.1/v2.2 EJTAG errata implemented.
- Using FASTDATA mode for CNMIPS II and XLP targets. This improves download/programming speed when using a WORKSPACE.

Error Correction

- Make all flash target algorithm position-independent. This fixes the problem that some of the programming algorithm failed when using a WORKSPACE.