

Config. program V1.00
Firmware for BDI2000 V1.01
Logic for BDI2000 V1.00

ABATRON

Date: March 13, 2009
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Enhancements

- Telnet command to display L1 data cache added (dtag / dcache).
- Multi-Threading debugging support improved (work-around for chip errata implemented).

Config. program V1.00
Firmware for BDI2000 V1.02
Logic for BDI2000 V1.00

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Date: Nov. 26, 2009
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Enhancements

- New Telnet „state“ command added. Shows information about all cores.
- Download speed via GDB improved.
- Maximal number of software breakpoints increased to 64.

Error Correction

- Error when target runs in little endian mode corrected.

Config. program V1.00
Firmware for BDI2000 V1.03
Logic for BDI2000 V1.00

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Enhancements

- Experimental support for „Non-stop Multi-Threaded Debugging in GDB“ added (see manual).
- New WREG init list entry allows to set PC, HI and LO registers.
- Flashprogramming now also via xkphys segment possible (64-bit address arithmetic).

Error Correction

- Make all flash target algorithm position-independent. This fixes the problem that some of the programming algorithm failed when using a WORKSPACE.