

Config. program      ■ V1.02  
Firmware for BDI3000   ■ V1.01

Date: March 13, 2009

## Enhancements

- Telnet command to display L1 data cache added (dtag / dcache).
- Multi-Threading debugging support improved (work-around for chip errata implemented).

Config. program  V1.02  
Firmware for BDI3000  V1.02

Date: Nov. 26, 2009

## Enhancements

- New Telnet „state“ command added. Shows information about all cores.
- Download speed via GDB improved.
- Maximal number of software breakpoints increased to 64.

## Error Correction

- Error when target runs in little endian mode corrected.

Config. program      ■ V1.03  
Firmware for BDI3000   ■ V1.03

Date: October 5, 2011

## Enhancements

- Maximal number of parallel handled cores increased to 32.
- Experimental support for „Non-stop Multi-Threaded Debugging in GDB“ added (see manual).
- New WREG init list entry allows to set PC, HI and LO registers.
- Flashprogramming now also via xkphys segment possible (64-bit address arithmetic).

## Error Correction

- Make all flash target algorithm position-independent. This fixes the problem that some of the programming algorithm failed when using a WORKSPACE.