

Config. program V1.00
Firmware for BDI2000 V1.01
Logic for BDI2000 V1.00



Date: May 24, 2002
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Enhancements

- New configuration parameter (CPUTYPE) added to define the target CPU type (see manual).
- Support for IOP321 and PXA2xx added.
- New Telnet command (RDACC / RMACC) to access internal accumulator added.
- Processing of GDB monitor command improved.
- Support for the 7 bit JTAG Instruction registers of IXP targets added.

Config. program V1.00
Firmware for BDI2000 V1.02
Logic for BDI2000 V1.00



Date: June 17, 2002
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Enhancements

- New configuration parameter (VTABLO, VTABHI) added to support fixed vector tables.
- No error returned to GDB when it tries to access invalid memory. This solves the problem when GDB refuses to connect to the target because it cannot read the stack frame.
- The „bkpt #1“ instruction triggers an automatic enter/exit debug mode sequence. This can be used to update the vector tables.

Error Correction

- The erase time-out for AMD flashes has been increased. This was necessary because of long chip erase time.
- Correct opcode for the relocated debug/reset vector loaded into Mini IC.

Config. program V1.01
Firmware for BDI2000 V1.03
Logic for BDI2000 V1.00



Date: January 27, 2003
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Enhancements

- Support for Multi-Core debugging added. Concurrent debugging of up to 4 daisy chained XScale cores with different GDB sessions is now possible.
- New configuration parameter (WAKEUP) added to support targets that needs some wake-up time after a reset.
- New configuration parameter (POWERUP) added to support targets that needs some wake-up time after a power-up.
- Support for network configuration via BOOTP added.
- Support for AMD MirrorBit flash added.
- Memory test function implemented (Telnet MT command).
- New Telnet command added to unlock multiple (same size, continuous) flash sectors with one command.
- Improved Telnet „erase“ command to erase multiple (same size, continuous) flash sectors with one command.
- Support for 32 bit wide flash chips added.
- Improved Telnet interface with history buffer and line editing.

Note:

The BDI's ethernet MAC address will change with this release.
The setup tool displays the new MAC address.

Config. program V1.01
Firmware for BDI2000 V1.04
Logic for BDI2000 V1.00



Date: Febr. 19, 2003
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Enhancements

- Workaround implemented that allows to change the CPU speed on a PXA250 without losing synchronization with the BDI2000.

Error Correction

- The last release (first one with multi-core support) introduces an error when selecting IOP310 as CPUTYPE. This has been corrected now.

Config. program V1.01
Firmware for BDI2000 V1.05
Logic for BDI2000 V1.00



Date: July 14, 2003
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Enhancements

- When using static vector tables, individual values can be defined for the different vectors. See description of VTABLO and VTABHI in the user's manual.
- The Telnet command „CONFIG“ allows to change the name of the configuration file name. The new name is stored in the BDI's flash and a boot sequence is started.
- New Telnet command (FLASH) added to change the flash configuration interactively. This may be useful when running a Telnet script to program different flash memory systems on the target. For example if there is a 8bit boot flash and a 32bit main flash system.

Error Correction

- Connecting problem to IXP2800 solved.
- Error in checksum calculation corrected (Telnet command MC).

Config. program ■ V1.02
Firmware for BDI2000 ■ V1.06
Logic for BDI2000 ■ V1.01



Date: April 30, 2004
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Enhancements

- The file names in the configuration file maybe relative to the configuration file path.
- The Telnet command „reset“ is now synchronous. This allows the use of it via a GDB monitor command. Also the handling of GDB monitor commands has been improved.
- The Telnet command „CONFIG“ allows to change the name and IP address of the configuration file. The new name and IP address is stored in the BDI's flash and a boot sequence is started.
- New Telnet DELAY command added. Maybe useful when working with GDB command scripts.
- Slower JTAG clock frequencies down to 100 kHz added.
- Support for new XScale CPU types added (IOP331, PXA270).

Config. program V1.02
Firmware for BDI2000 V1.07
Logic for BDI2000 V1.01



Date: June 25, 2004
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Enhancements

- A new init list entry „FILL“ allows to fill a memory range. This maybe useful to initialize an ECC protected memory range.
- The Telnet command „FILL“ allows to fill a memory range.
- The Telnet command „CONFIG“ allows to change all BDI network parameters. The new parameters are stored in the BDI's flash and a boot sequence is started.

Error Correction

- Correct reset/startup sequence for PAX270 implemented.

Config. program V1.02
Firmware for BDI2000 V1.08
Logic for BDI2000 V1.03



Date: Nov. 19, 2004
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Enhancements

- A list of valid memory ranges can be defined to avoid invalid memory accesses via the JTAG interface (MMAP entry in the init list).
- Driver type of the TRST output can be configured as open-drain or push-pull.
- The time how long the BDI asserts the reset signal can be configured.
- Support for IXP23xx added.
- Telnet history buffer no longer filled with repeated commands.
- The erase list accepts a new UNLOCK option (see user's manual).

Config. program V1.03
Firmware for BDI2000 V1.09
Logic for BDI2000 V1.03



Date: January 6, 2006
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Enhancements

- Trap mask (vector catch) can be changed via the new Telnet TRAP command.
- The flash programming algorithms drain the write buffer.
- The Windows setup tools now allows to select a COM port in the range 1 - 19.
- The Telnet CONFIG command displays the firmware version.

Error Correction

- Correct single step of the BX PC instruction.

Config. program V1.03
Firmware for BDI2000 V1.10
Logic for BDI2000 V1.03



Date: March 17, 2006
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Enhancements

- Enhanced flash erase support via configuration file (see user's manual).
- Support for Monahans added.
- For processors based on the Manzano core, information about the L1/L2 cache can be displayed via the new Telnet commands DCACHE, ICACHE and L2TAG.
- The BDI no longer asserts RESET during target power-up.

Config. program V1.03
Firmware for BDI2000 V1.11
Logic for BDI2000 V1.03



Date: March 16, 2007
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Enhancements

- Improved Telnet history buffer handling.
- New flash algorithm types added (S29M64X8, S29M32X16).
- The MirrorBit algorithm uses the write buffer also when no workspace is defined.
- New RECONNECT option to the DEBUGPORT parameter added. When present, a Telnet/GDB (TCP) connection request to an already open TCP channel is accepted if the host IP is the same as the initial one. The old TCP channel will be closed immediately.
- An new option allows to define if the BDI should asserts RESET during target power-up
- Maximal number of devices on the JTAG scan chain increased.

Config. program V1.03
Firmware for BDI2000 V1.12
Logic for BDI2000 V1.03



Date: October 1, 2009
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Enhancements

- Telnet accepts now multiple commands separated by a semi-colon on one line.
- Download speed via GDB improved.
- Telnet IAC option handling improved.

Error Correction

- Correct single step of newer ARMv6/v7 instructions.